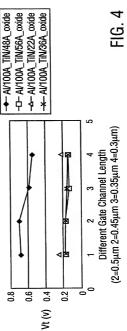


Vt(v) for various gate stack with different TiN thickness



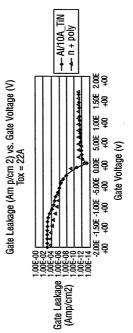


FIG. 5

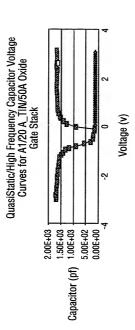


FIG. 6

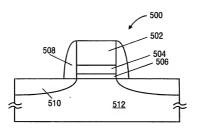


FIG. 7

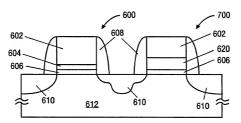


FIG. 8